SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

M.Tech III Semester (R20/R21) First Mid Term Examinations

TIME TABLE

Timings: 10.00 a.m. to 11.30 a.m.　　　　　　　　　　　　　　　　**27th August 2024**

Date	Structural Engineering	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning) *(R21 Regulation)*
02.09.2024 (Monday)	Earthquake Resistant Design of Structures (CE3921)	Lower Power VLSI Design (EC4922)	Recommender Systems (CS4923)
03.09.2024 (Tuesday)	Sustainable Development (CE3924)	Sustainable Development (CE3924)	Sustainable Development (CE3924)

Note: Seating arrangement will be notified before the commencement of the examination by the respective departments.

Vice Principal
(Academics)

Copy to: HODs – CE/ECE/CSE/CoE

　　　　　Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)